

HARSH SHARMA

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EDUCATION

Ph.D. Candidate, Computer Engineering, 3.93 GPA **2021–Present**
Advisors: Partha Pratim Pande & Janardhan Rao Doppa

Washington State University

Pullman, Washington

Courses: • Advanced Computer Architecture • Machine Learning • Computational Genomics

• Neural Network Design & Analysis • SoC Design and Test • VLSI Systems Design

Bachelor of Engineering, Electronics and Communication Engineering **2017–2021**

NSIT, Delhi University

New Delhi, India

Department Ranker (Top 5%)

Coursework: • Artificial Intelligence • Microprocessors • Operating Systems • Project Management

EXPERIENCE

Machine Learning Research Intern **June 2020–December 2020**

Lenskart.com

New Delhi, India

Developed AR tools (in production) with vision model to boost online sales by 35%.

SELECTED PUBLICATIONS

- H. Sharma**, L. Pfromm, J. Doppa, U. Y. Ogras, A. Kalyanraman, P. Pande. Network-on-Interposer Design for CNN Inferencing in Presence of Defects. *In Progress*, 2023.
- [Best Paper Award]** **H. Sharma**, L. Pfromm, R. Topaloglu, J. Doppa, U. Y. Ogras, A. Kalyanraman, P. Pande. Florets for Chiplets: Data Flow-aware High-Performance and Energy-efficient Network-on-Interposer for CNN Inference Tasks. *ESWEEK*, 2023.
- H. Sharma**, S. K. Mandal, J. Doppa, U. Y. Ogras, P. Pande. Achieving Datacenter-scale Performance through Chiplet-based Manycore Architectures. *DATE*, 2023.
- [Best Paper Award]** **H. Sharma**, S. K. Mandal, J. Doppa, U. Y. Ogras, P. Pande. SWAP: A Server-Scale Communication-Aware Chiplet-Based Manycore PIM Accelerator. *ESWEEK*, 2022.
- H. Sharma**, D. Gadre, S. Gadre, S. Srivastava. Science on a stick: An experimental and demonstration platform for learning several physical principles. *American Journal of Phys.*, 2022.

AWARDS AND HONORS

- National Science Foundation (NSF) Travel Grant, 2023
- **Best Paper Award** at ACM/IEEE Embedded Systems Week Conference, 2023 [§]
- **Best Paper Award** at ACM/IEEE Embedded Systems Week Conference, 2022 [†]
- DAC Richard Newton Young Fellow, 2022

SELECTED PROFESSIONAL AND OUTREACH ACTIVITIES

Conferences and Invited Talks

- SWAP: A Server-scale Communication aware Chiplet-based PIM Accelerator at ESWEEK 2022.
- Achieving Datacenter-scale Performance through Chiplet-based Manycore Architectures at DATE 2023.
- Florets for Chiplets: Data Flow-aware High-Performance & Energy-efficient Network-on-Interposer for CNN Inference Tasks at Hamburg, Germany; ESWEEK-2023.
- Talk on *AI-Driven Design & Optimization of Chiplet-Systems for Server-Scale Applications* Pullman-2023.
- Talk on *AI-Driven Design & Optimization strategies for more Moore* at NSIT Delhi (Virtual)-2023.
- Talk on *Accelerating the Future of Electronics* at Boston University -2023. [‡]

Reviewer

- ESWEEK 2022, ICCAD 2023, DAC 2022, DAC 2023, DATE 2022

SKILLS

- **Programming Languages.** Python, Bash, C/C++, HTML/CSS, L^AT_EX, MATLAB, JavaScript
- **Tools/Packages.** Git, SQL, PyTorch, TensorFlow, Python data science tools, Arduino, TI MSP430G253

[§]<https://news.wsu.edu/news/2023/10/11/researchers-receive-best-paper-award/>

[†]<https://school.eecs.wsu.edu/2022/10/14/cases-best-paper-award/>

[‡]Based on <https://medium.com/@harshari/accelerating-the-future-of-electronics-e23cc42d9d39>